

09/893,188

F0654

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of Claims:**

1. (Previously presented) A method for making a dual damascene pattern in a single etch process comprising:
  - providing a wafer having at least one insulative layer formed thereon;
  - depositing a first photoresist layer over the at least one insulative layer;
  - patterned a first image into the first photoresist layer;
  - curing the first patterned photoresist layer;
  - depositing a second photoresist layer over the first patterned photoresist layer;
  - patterned a second image into the second photoresist layer; and
  - etching the at least one insulative layer through the first patterned photoresist layer and the second patterned photoresist layer simultaneously in the single etch process, wherein the first image and the second image are substantially formed in the at least one insulative layer.
2. (Original) The method of claim 1, wherein curing the first patterned photoresist layer comprises irradiating the first patterned photoresist layer with ultraviolet light.
3. (Original) The method of claim 2, wherein the ultraviolet light irradiates the first patterned photoresist layer for a time and at an energy dose sufficient to make the first patterned photoresist chemically resistant to organic solvents and developers.
4. (Previously presented) The method of claim 1, wherein etching the

09/893,188

F0654

at least one insulative layer through the first patterned photoresist layer and the second patterned photoresist layer further comprises employing an etch chemistry that ablates an amount of the first patterned photoresist layer during the etching process without substantially affecting the second patterned photoresist layer.

5. (Previously presented) The method of claim 4, wherein the etch chemistry is highly selective to the first patterned photoresist layer and to the at least one insulative layer than to the second patterned photoresist layer.

6. (Original) The method of claim 1, further comprising removing the first patterned photoresist layer and the second patterned photoresist layer.

7. (Original) The method of claim 1, wherein the first patterned photoresist layer is a positive tone photoresist layer.

8. (Original) The method of claim 1, wherein the second patterned photoresist layer is a negative tone photoresist layer.

9. (Original) A method for making a dual damascene pattern using a dual layer patterning scheme and a single etch process comprising:

providing a wafer having at least one insulative layer formed thereon;

depositing a first photoresist layer over the at least one insulative layer;

patterning a first image in the first photoresist layer;

irradiating the first patterned photoresist layer with ultraviolet light to stabilize the first patterned photoresist layer;

depositing a second photoresist layer on the first patterned photoresist layer;

patterning a second image in the second photoresist layer;

09/893,188

F0654

etching the at least one insulative layer through the first patterned photoresist layer and the second patterned photoresist layer simultaneously in the single etch process, wherein the first image and the second image are substantially formed in the at least one insulative layer; and

removing the first patterned and the second patterned photoresist layers.

10. (Original) The method of claim 9, wherein irradiating the first patterned photoresist layer with ultraviolet light is for a time and at an energy dose sufficient to make the first patterned photoresist chemically resistant to organic solvents and developers.

11. (Previously presented) The method of claim 9, wherein etching the at least one insulative layer through the first patterned photoresist layer and the second patterned photoresist layer further comprises employing an etch chemistry that ablates an amount of the first patterned photoresist layer during the etching process without substantially affecting the second patterned photoresist layer.

12. (Previously presented) The method of claim 11, wherein the etch chemistry is highly selective to the first patterned photoresist layer and to the at least one insulative layer than to the second patterned photoresist layer.

13. (Original) The method of claim 9, wherein the first patterned photoresist layer is a positive tone photoresist layer.

14. (Original) The method of claim 9, wherein the second patterned photoresist layer is a negative tone photoresist layer.

15. (Previously presented) A method for making a dual damascene pattern using a dual layer patterning scheme comprising:

09/893,188

F0654

providing a wafer having at least one insulative layer formed thereon;

depositing a positive tone photoresist layer over the at least one insulative layer;

patterned a first image in the positive tone photoresist layer;

irradiating selected portions of the positive tone photoresist through a mask to effect an image-wise pattern transfer, wherein irradiated portions of the positive tone photoresist layer are removed;

polymerizing first patterned photoresist layer using ultraviolet light radiation;

depositing a negative tone photoresist layer over patterned positive tone photoresist layer;

irradiating selected portions of the negative tone photoresist through a mask to effect an image-wise pattern transfer, wherein non-irradiated portions of the negative tone photoresist layer are removed;

etching the at least one insulative layer through the patterned positive tone photoresist layer and the patterned negative photoresist layer simultaneously in a single etch process, wherein the first image and the second image are substantially formed in the at least one insulative layer; and

removing the patterned positive tone and the patterned negative tone photoresist layers.

16. (Previously presented) The method of claim 15, wherein polymerized portions of the positive tone photoresist layer are chemically resistant to standard developer solutions and organic solvents.

17. (Previously presented) The method of claim 15, wherein etching the at least one insulative layer through the patterned negative tone photoresist layer and the patterned positive tone photoresist layer further comprises employing an etch chemistry that ablates an amount of the patterned positive tone photoresist

09/893,188

F0654

---

layer during the etching process without substantially affecting the patterned negative tone photoresist layer.

18. (Previously presented) The method of claim 17, wherein the etch chemistry is highly selective to the patterned negative tone photoresist layer and to the at least one insulative layer than to the patterned positive tone photoresist layer.